## Practitioner's Docket No. MST-013-1D

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Fu-Chieh Hsu

Application No.: Filed Herewith Parent Appln. Group No.: 2815

Filed: 9/30/2003 Parent Appln. Examiner: Allan R. Wilson

For: "One-Transistor Floating-Body DRAM Cell In Bulk CMOS Process

With Electrically Isolated Charge Storage Region"

Commissioner for Patents-P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

## List of Sections Forming Part of This Information Disclosure Statement

The following sections are being submitted for this Information Disclosure Statement:

 Identification of Prior Application in Which Listed Information Was Already Cited and for Which No Copies Are Submitted or Need Be Submitted

## Section 1. Identification of Prior Application in Which Listed Information Was Already Cited and for Which No Copies Are Submitted or Need Be Submitted

This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior application Serial No.: 10/095,901, filed on March 11, 2002.

Copies of the documents listed on the accompanying Form PTO-1449 that are not enclosed were previously submitted in Application No. 10/095,901, from which this Application claims an earlier effective filing date.

Applicant respectfully requests that the listed information be considered by the Examiner and be made of record in the above-identified application. Form PTO-1449 is enclosed and the Examiner is requested to initial and return it in accordance with MPEP § 609.

This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56.

$\boxtimes$	This statement qualifies under 37 C.F.R. § 1.97, subsection (b) because (check all that apply):								
	$\boxtimes$	(1)	It is being filed within 3 months of the application filing date and is other than a continued prosecution application under § 1.53(d)						
		(2)	OR It is being filed within 3 months of entry of a national stage OR						
		(3)	It is being filed before the mail date of the first Office Action on the merits.						
		(4)	It is being filed before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114						

Date: September 30, 2003

Atty. Dkt. No.: MST-013-1D SN: Filed Herewith

37 C.F.R. § $1.97(c)$ . If this statement is being filed after the period specified in § $1.97(b)$ , but before the mailing date of the earlier of a final office action under § $1.113$ , a notice of allowance under § $1.311$ , or a action that otherwise closes prosecution in the application, then:						
	a certification as specified in § 1.97(e) is provided below; or					
	a fee of \$180.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.					
37 C.F.R. § 1.97(d). If this statement is being filed after the period specified in § 1.97(c), but on or before payment of the issue fee, then:						
A.	a certification as specified in § 1.97(e) is completed below; and					
В.	a fee of \$180.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.					
Fee Authorization. The Commissioner is hereby authorized to charge the above-referenced fees of \$180 and charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0574 (Docket No).						

Date: 9-30-03

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E. Eric Hoffman

Bever, Hoffman & Harms, LLP

I hereby certify that this correspondence is being deposited with the United States Postal Service as EXPRESSMAIL Label No.: EO901670125US, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-

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Substitute for form 1449A/PTO  INFORMATION DISCLOSURE STATEMENT BY APPLICANT						oplication Number	Filed Herewith				
					Filing Date		9/30/2003				
					First Named Inventor:		Fu-Chieh Hsu				
(use as many sheets as necessary)					Examiner name: A. Wilson (Parent case)		GROUP: 2815 (Parent case)				
Sheet 1 of 1			1	Attorney Docket Number		MST-013-1D					
<u> </u>				U.	S. P	ATENT DOCUMENTS			<u></u>		
Examiner Initials*						nme of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Where Relevant Appear			
		5937296			Arnold		08-10-1999				
		6281557		B1	Trueby et al.		08-28-2001				
	A3	5739567		-	Wong		04-14-1998				
	A4	521626			Middelhoek et al.		06-01-1993				
	A5	5264716			Kenney		11-23-1993				
	A6	5672536			Wu et al.		09-30-1997				
	A7	5708290			Cacciola et al.		01-13-1998				
	A8	5789286		Subbanna		ibbanna	08-04-1998				
	J			FO	REI	GN PATENT DOCUMENTS					
Examiner Initials*	Cited No. 1	01100			de <sup>5</sup>	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear			
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	Articl	e entitled	"A Canac	itor-Less	<u>3 (11</u> 1T-I	DRAM Cell", IEEE Electron De	evice Letters, Vol. 2	3, No. 2, Feb. 2002	, by S		
	Okho	nin et al.,	pages 85-	37							
	Artic Ferro	Article entitled "Memory Design Using One-Transistor Gain Cell on SOI" ISSCC 2002/Session 9/DRAM And Ferroelectric Memories/9.1; by Takashi Ohsawa et al.; pages 452-454.									
EXAMIN	ER					DATE CONSIDERED  t citation is in conformance with MPEP 609; draw line through citation if not in conformance and not					

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.